

Fuzzy 945GM1 / 945GME1 Series

MS-7265(V3.X) Mainboard



G52-72651X2

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Revision History

| Revision | Revision History | Date |
|----------|-----------------------|-------------|
| V3.0 | First release for IPC | August 2007 |

Technical Support

If a problem arises with your system and no solution can be obtained from the user's manual, please contact your place of purchase or local distributor. Alternatively, please try the following help resources for further guidance.

Visit the MSI website for FAQ, technical guide, BIOS updates, driver updates, and other information: http://global.msi.com.tw/index.php? func=faqIndex

Contact our technical staff at: http://support.msi.com.tw

Safety Instructions

- 1. Always read the safety instructions carefully.
- 2. Keep this User's Manual for future reference.
- 3. Keep this equipment away from humidity.
- 4. Lay this equipment on a reliable flat surface before setting it up.
- The openings on the enclosure are for air convection hence protects the equipment from overheating. DO NOT COVER THE OPENINGS.
- Make sure the voltage of the power source and adjust properly 110/220V before connecting the equipment to the power inlet. Rating: 100-127/200-240V-, 4/2A, 60/50Hz.
- Place the power cord such a way that people can not step on it. Do not place anything over the power cord.
- 8. Always Unplug the Power Cord before inserting any add-on card or module.
- 9. All cautions and warnings on the equipment should be noted.
- 10. Never pour any liquid into the opening that could damage or cause electrical shock.
- 11. If any of the following situations arises, get the equipment checked by service personnel:
 - † The power cord or plug is damaged.
 - † Liquid has penetrated into the equipment.
 - † The equipment has been exposed to moisture.
 - † The equipment does not work well or you can not get it work according to User's Manual.
 - † The equipment has dropped and damaged.
 - † The equipment has obvious sign of breakage.
- DO NOT LEAVE THIS EQUIPMENT IN AN ENVIRONMENT UNCONDITIONED, STOR-AGE TEMPERATURE ABOVE 60°C (140°F), IT MAY DAMAGE THE EQUIPMENT.



CAUTION: Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer.

警告使用者:

這是甲類的資訊產品,在居住的環境中使用時,可能會造成無線電干擾, 在這種情況下,使用者會被要求採取某些適當的對策。



廢電池請回收

For better environmental protection, waste batteries should be collected separately for recycling or special disposal.

FCC-B Radio Frequency Interference Statement

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part



15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the measures listed below.

- † Reorient or relocate the receiving antenna.
- † Increase the separation between the equipment and receiver.
- † Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- † Consult the dealer or an experienced radio/television technician for help.

Notice 1

The changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Notice 2

Shielded interface cables and A.C. power cord, if any, must be used in order to comply with the emission limits.

VOIR LANOTICE D'INSTALLATION AVANT DE RACCORDER AU RESEAU.



This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

(1) this device may not cause harmful interference, and

(2) this device must accept any interference received, including interference that may cause undesired operation.

WEEE (Waste Electrical and Electronic Equipment) Statement



ENGLISH

To protect the global environment and as an environmentalist, MSI must remind you that...

Under the European Duion ("PLU") Directive on Waste Electrical and Electronic Equipment, Directive 2002/86/EC, which takes effect on August 13, 2005, products of "electrical and electronic equipment" cannot be desarded as multipal waste anymore and manufacturers of evered electronic equipment with the obligated to take back such products at the end of their useful life. MST will emply with the product take back requirements at the end of life of MSL-branded products that are sold into the EU. You can return these products to local collection points.

DEUTSCH

Hinweis von MSI zur Erhaltung und Schutz unserer Umwelt

Gemäß der Richtlinfe 2002/96/F.C über Elektro- und Elektronik-Altgeräte dürfen Elektro- und Elektronik-Altgeräte nicht mehr als kommunale Abfülle entsorgt werden. MSI hat europaweit verschiedene Sammel- und Recyclingunternehmen heauftragt, die in die Europätsche Union in Verkehr gebrachten Produkte, am Ende seines Lebenszyklus zurückzunehmen. Bitte entsorgen Sie dieses Produkt zum gegebenen Zeitpunkt ausschliesslich an einer lokalen Altgerätesammelstelle in Ihrer Nähe.

FRANÇAIS

En tant qu'écologiste et afin de protégor l'environnement, MSI tient à rappeler ceci,..

Au sujet de la directive curopécene (RU) relative aux déchets des équippement électriques et électroniques, directive 2002/96/EC, prenant effet le 13 août 2005, que les produits électriques et électroniques, ne peuvent éré déposé dans les décharges ou tout simplement mis à la poubelle. Les fabricants de ces équipments seront obligés de récupérer certains produits en fin de vie. MSI prendra en compte ectte exigence relative au retuur des produits en fin de vie au sein de la communanté curopéenne. Par conséquent vous pouvez retourner localement ces matéries dans les points de collecte.

РУССКИЙ

Компания MSI предпринимает активные действия по защите окружающей среды, поэтому напоминаем вам, что....

В соответствии с директивой Европейского Союза (ЕС) по прелотвращению загрязшения окружающей среды использованных электрических и электронных оборудованием (директивя WEEE 2002/06/С/), вступающей в силу 13 августа 2005 года, идления, отновениисся и электрическому и электронному оборудование, не могут рассмятриваться как бытовой мусов, ноэтому промноолители вышеперечисленного электронного оборудования обязавы принямать его аля переработки по окончании срока службы. МSI обязустся соблюдать пребования но присукции, проданной под маркой MSI на территории ЕС, в переработку по окончания срока службы. Вы можете верпуть эти изделия в специализированных пункты присма.

ESPAÑOL

MSI como empresa comprometida con la protección del medio ambiente, recomienda:

Bajo la directiva 2002/96/FC de la l'infan Farapea en mareria de desechas y/o equipos electránicos, con fecha de rigor desde el 13 de agosto de 2005, los productos clasificados como "eléctricos y equipos electrónicos" no poreden ser depositados en los contenencedores habituales de su manicipio, los fabricantes de equipos electrónicos, están obligados a hacerse cargo de dichos productos al termino de su periodo de vida. MSI estará comprometido con los términos de recogida de sus productos en la Unión Europea al final de su periodo de vida. Usied debe depositar estos productos en el punto limpio establecido por el ayuntamiento de su localidad o entregar a una compresa autorizada para la recegida de estos reciduos.

NEDERLANDS

Om het milieu te beschermen, wil MSI u eraan herinneren dat....

De richtlijn van de Føropese Unic (EU) met betrekking tot Vervuiling van Electrische en Electronische producten (2002/96/EC), die op 13 Augustus 2005 in zal gaan kunnen niet meer beschouwd worden ab vervuiling.

Fabrikanten van dit souert producten worden verplicht om producten retour te memen aan het eind van han levenseyelus. MSI zal overeenkomstig de richtlijn handelen voor de producten die de merknaam MSI dragen en verkenkt dijn in de EU. Deze goederen kunnen geretourmeerd worden op lokale inzamelingspunten.

SRPSKI

Da bi zaštitili prirodnu sredinu, i kao preduzeće koje vodi računa o okolini i prirodnoj sredini, MSI mora da vas podesti da...

Po Direktivi Evropske unije ("EU") o odbačenoj ekektronskoj i električnoj opremi. Direktiva 2002/96/EC, koja stupa na snagu od 13. Avgusta 2006, proizvodi koji spađaju pod "alektronsku i električnu opremu" na mogu vito biti odbačeni kau obizan otpad i preizvodači sve opreme biče prinudeni od uzvna nartago use proizvoda na kraju njihovog unbičajenog veka trajanja. MSI će poštovati zahtev o preuzimanju ovakvih proizvoda kojima je fstekao vek trajanja, koji imaju MSI oznaku i koji su prodati u EU. Ove proizvode možete vratiti na lokalnim mestima za prikupljanja.

POLSKI

Aby chronić nasze środowisko naturalne oraz jako firma dbająca o ekologię, MSI przypomina, że...

Zgodnie z Dyrektywą Unii Europcjskiej ("UE") dotyczącą odpadów produktów elektrycznych i elektronicznych (Dyrektywa 2002/96/EC), która wchodzi w życie 13 sierpnia 2005. tzw. "produkty oraz wyposażenie elektryczane i oktroniczew " nie mogą być traktowane jako śmieci komanolne, tak wię producenej tych produktów hodą zobowiązani do odbierania ich w momencie gdy produkt jest wycofywany z użycia. MSI wypelni wymagania UE, przyżmując produkty (sprzedawane na terenie Unii Europcjskiej) wycofywane z użycia. Produkty MSI bedzie można zwraceń w wyznaczonych punktach dbierzach.

TÜRKÇE

Cevreci özelliğiyle bilinen MSI dünyada çevreyi korumak için hatırlatır:

Avrupa Birliği (AB) Kararnamesi Etektrik ve Elektronik Malzene Arigi, 2002/20/EC Kararnamesi alında 13 Ağustos 2005 tarihinden itibaren geçerli olmak üzere, elektrikli ve elektronik malzemeler diğer atıklar gibi çöpe atılanayacak ve bu elektonik dihazların üzetlerler, elmakarın kullanım sürekeri hittiken sonra üründeri gor toplamakla yükümlü olacaktır. Avrupa Birliği'ne satılan MSI markah ürünkerin kullanım süreleri bittiğinde MSI ürünlerin geri alınmaşı İsteği Ile İşbirliği içerisinde olacaktır. Ürünlerinizi yerel toplama noktalarına bırakabilirsiniz.

ČESKY

Záleží nám na ochraně životního prostředí - společnost MSI upozorňuje...

Podle směrnice Evropské unic ("EU") o ilkvidaci clektrických a clektronických výrobků 2002/96/EC platné od 13. srpna 2005 je zakázáno likvidovat "clektrické a člektronické výrobky" v běžněm komunálním odpadu a výrobci clektronických výrohků, na které se tato směrnice vztahuje, budou povinni odchirat takové výrobky zpět po skončení jejich životnosti. Společnost MSI splní požadavky na odchirání výrobků změtky MSI, prodávaných v zmích EU, po skončení jejich životnosti. Tyto výrohky měžete odevzdat v místních skěrnách.

MAGYAR

Annak érdekében, hogy környezetűnket megvédjűk, illetve környezetvédőként fellépve az MSI emlékezteti Önt, hogy ...

Az Európal Ulió ("EU") 2005. augusztus 13-án hatályba lépő, az elektromise és deletronikus berendezések hullaldékairól szóló 2002/96/EK irányelve szorint az elektronikus berendezések többé nem kezethetőki takossági hulladékkört, és nz ilyen elektronikus berendezések gyáról kötelses év válnak az ilyen termékek visszavételére azok hasznos élettartama végén. Az MSI betartja a termékvisszavétellel kapcsolatus kövel. Az MSI márkanév alatti az EU-a belli értéksített termékek esetében, azok élettartamának végén. Az ilyen termékeket a legközzebbő ígyítíkohyre viheti.

ITALIANO

Per proteggere l'ambiente, MSI, da sempre amica della natura, ti ricorda che....

In base alla Direttiva dell'Unione Europea (EU) sullo Smaltimento dei Materiali Elettrici ed Elettronici, Direttiva 2002/96/EC in vigore dal 13 Agosto 2005, prodotti appartenenti alla categoria dei Materiali Elettrici ed Elettronici no possono più escere clininati come riftuit municipali: i produttori di detti materiali saranno obbligati a ritirare ugni produtto alla fine del suo ciclo di vita. MSI si adeguerà a tale Direttiva ritirando tutti i proshibi portato i produtto il di vinduti all'interno dell'Unione Europea alla fine del loro ciclo di vita. È possibile portato i produtto i più vicino pauto di raccolta.

CONTENTS

| Copyright Notice | ii |
|--|-----|
| Trademarks | .ii |
| Revision History | ii |
| Technical Support | ii |
| Safety Instructionsi | iii |
| FCC-B Radio Frequency Interference Statementi | v |
| WEEE (Waste Electrical and Electronic Equipment) Statement | v |
| Chapter 1 Getting Started1- | 1 |
| Mainboard Specifications1- | 2 |
| Block Diagram1- | 4 |
| Mechanical Drawing1- | 5 |
| Back Panel and I/O Shield Drawing1- | 6 |
| Mainboard Layout1- | 7 |
| Packing Checklist1- | 8 |
| Chapter 2 Hardware Setup2- | 1 |
| Quick Components Guide2-2 | 2 |
| CPU (Central Processing Unit)2- | 3 |
| Memory2- | 6 |
| Power Supply2- | 7 |
| Back Panel2- | 8 |
| Connectors2-1 | 0 |
| Jumper2-1 | 5 |
| Slot2-1 | 6 |
| Chapter 3 BIOS Setup3- | 1 |
| Entering Setup3- | 2 |
| The Main Menu | 4 |
| Standard CMOS Features | 6 |
| Advanced BIOS Features | 9 |
| Advanced Chipset Features | 1 |
| Integrated Peripherals | 2 |
| Power Management Setup3-1 | 5 |
| PNP/ PCI Configurations | 7 |
| PC Health Status | 8 |
| Frequency/ Voltage Control | 9 |
| Load Fail-Safe/ Optimized Defaults | 0 |
| Set Supervisor/ User Password | 1 |

| Chapter 4 System Resources | .4-1 |
|---------------------------------|------|
| Watchdog Timer Setting | 4-2 |
| Award POST Code | 4-5 |
| Check Point & Beep Code List | 4-11 |
| PCIConfiguration | 4-18 |
| Resource List | 4-19 |
| Appendix A Realtek ALC655 Audio | .A-1 |
| Installing the Audio Driver | A-2 |
| Software Configuration | A-4 |
| Hardware Setup | A-11 |

Chapter 1 Getting Started

Thank you for choosing the Fuzzy 945GM1 / 945GME1 Series (MS-7265 V3.X) Mini-ITX mainboard. The Fuzzy 945GM1 / 945GME1 Series mainboards are based on Intel® 945GM / 945GME and Intel® ICH7-MDH chipsets for optimal system efficiency. The Fuzzy 945GM1 / 945GME1 Series deliver a high performance and professional desktop platform solution.



Mainboard Specifications

Processor Support

- Supports Intel[®] Yonah (Intel[®] Core™ Duo/ T2000 Seqence) and Merom Napa Reflash (Intel[®] Core™ 2 Duo/ T5000 & T7000 Sequence) processor up to 2.33GHz (Napa Platform) in PGA Package

- 2MB L2 cache (Yonah)/ 4MB (Merom)
- Supports 3 pin CPU Fan Pin-Header with Fan Speed Control
- Supports EIST Technology
- Supports Intel[®] Core™ Microarchitecture

Supported FSB

- 533/ 667 MHz

Chipset

- North Bridge: Intel® 945GM/ 945GME chipset
- South Bridge: Intel® ICH7-MDH (82801GHM) chipset

Memory Support

- DDR2 533/ 667 SDRAM (4GB Max)
- 2 DDR2 DIMMs (240pin / 1.8V)

LAN

- Supports Dual Intel® 10/100/1000 LAN by two Intel® 82541PI GigaLAN controller

Audio

- Chip integrated by Realtek®ALC655
- Flexible 5.1-channel audio
- Compliant with AC97 Spec

IDE

- 1 IDE port by Intel® ICH7-MDH
- Supports Ultra DMA 66/ 100 mode
- Supports PIO, Bus Master operation mode

SATA

- 2 SATA ports by Intel® ICH7-MDH
- Supports two SATA devices
- Supports storage and data transfers at up to 150 MB/s

RAID

- SATA1~2 support RAID 0/ 1/ JBOD mode by Intel® ICH7-MDH

Floppy

- 1 floppy port
- Supports 1 FDD with 360KB, 720KB, 1.2MB, 1.44MB and 2.88MB

Connectors

Back Panel

- 1 PS/2 mouse port
- 1 PS/2 keyboard port
- 2 serial ports
- 4 USB 2.0 ports
- 2 LAN jacks
- 3 flexible audio jacks
- 1 VGA port
- 1 DVI port

• On-Board Pinheaders/ Connectors

- 1 Aux Line-In connector
- 2 USB 2.0 pinheaders
- 1 Front Panel Audio pinheader

Slots

- 1 PCI v2.3 slot (for standard PCI add-on card)

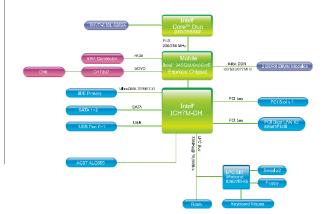
Form Factor

- Mini-ITX (17cm x 17cm)

Mounting

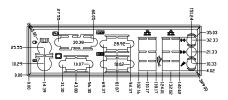
- 4 mounting holes

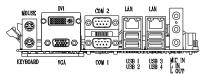
Block Diagram

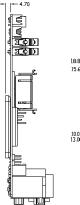


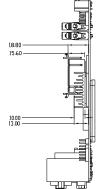
Mechanical Drawing 10.16¥ 22.86 202 X 77,50 154.94 160.02 152.50× \$ ×^{157.48} 8 151.83 ٦ 122.43 šX 8 107.50 <u>@</u>[E F 96.06~ 8 84.25× Ĭ •× 2040 59.04 56.05 43.46 34.20 🖌 S B **氏** 回 14.98 E 12.46 ×0.00 0.00 ® 1983 Þ PCI Þ ⁶³⁵× × 1016 ×sana ×nıs ×92.03 ×106.68

Back Panel and I/O Shield Drawing



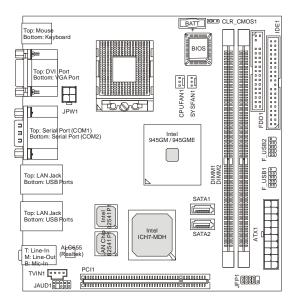




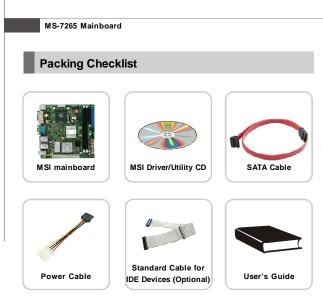


Getting Started

Mainboard Layout



Fuzzy 945GM1 / 945GME1 Series (MS-7265 V3.X) Mini-ITX Mainboard



* The pictures are for yuour reference only. Your packing contents may vary depending on the model you purchased.

Chapter 2 Hardware Setup

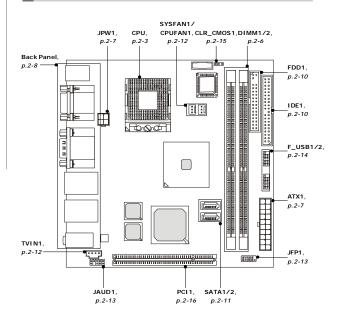
This chapter provides you with the information about hardware setup procedures. While doing the installation, be careful in holding the components and follow the installation procedures. For some components, if you install in the wrong orientation, the components will not work properly.

Use a grounded wrist strap before handling computer components. Static electricity may damage the components.

ONLY FOR SERVICE PERSONEL Always unplug the power cord before inserting any add-on card or module.



Quick Components Guide



CPU (Central Processing Unit)

The mainboard supports Intel[®] Yonah (Intel[®] Core[™] Duo' T2000 Sequence) and Merom Napa Reflash (Intel[®] Core[™] 2 Duo' T5000 & T7000 Sequence) in PGA Package. When you are installing the CPU, make sure the CPU has a heat sink and a cooling fan attached on the top to prevent overheating. If you do not have the heat sink and cooling fan, contact your dealer to purchase and install them before turning on the computer.



- Overheating will seriously damage the CPU and system. Always make sure the cooling fan can work properly to protect the CPU from overheating.
- Make sure that you apply an even layer of heat sink paste (or thermal tape) between the CPU and the heatsink to enhance heat dissipation.
- While replacing the CPU, always turn off the power supply or unplug the power supply's power cord from the grounded outlet first to ensure the safety of CPU.

MS-7265 Mainboard

CPU & Cooler Installation for PGA Package

1. Locate the CPU socket on the mainboard.



- 2. Place the CPU on top of the socket. Make sure to align the gold arrow on the CPU with the arrow key on the socket.
- 3. Push the CPU down until its pins securely fit into the socket.



4. On the front end of the CPU socket is a locking mechanism designed into the form of a screw head. Make sure that you actuate or deactuate this mechanism with a screwdriver before and after installing the CPU.





- 5. Flip over the mainboard and locate the position of the CPU socket.
- Detach the shield of the CPU cooler backplate paste and install the backplate to the back of the CPU socket with holes aligned.





- 7. The heatsink paste helps to enhance heat dissipation of the CPU. Before installing the cooler set (fan & heatsink bundled), make sure that you detach the shield of the heatsink paste under the cooler set.
- Locate the four screw holes around the CPU socket where the CPU cooler backplate was installed. Align the cooler set with the screw holes and mount it on top of the CPU.

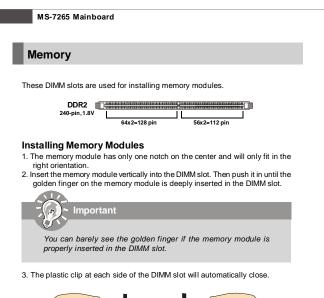


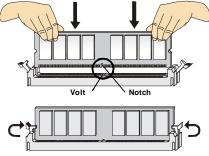


- 9. Screw to secure the cooler set to the mainboard.
- 10. Connect the fan power cable to the CPUFAN1 connector on the mainboard.









Power Supply

ATX 20-Pin Power Connector: ATX1

This connector allows you to connect to an power supply. To connect to the power supply, make sure the plug of the power supply is inserted in the proper orientation and the pins are aligned. Then push down the power supply firmly into the connector.



| Pin Definition | | | | | | |
|----------------|----------------------|----|-------|--|--|--|
| PIN | SIGNAL PIN PINSIGNAL | | | | | |
| 1 | 3.3V | 11 | 3.3V | | | |
| 2 | 3.3V | 12 | -12V | | | |
| 3 | GND | 13 | GND | | | |
| 4 | 5V | 14 | PS_ON | | | |
| 5 | GND | 15 | GND | | | |
| 6 | 5V | 16 | GND | | | |
| 7 | GND | 17 | GND | | | |
| 8 | PW_OK | 18 | -5V | | | |
| 9 | 5V_SB | 19 | 5V | | | |
| 10 | 12V | 20 | 5V | | | |

ATX 12V Power Connector: JPW1

This 12V power connector is used to provide power to the CPU.

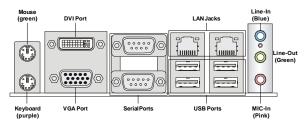


| Pin Definition | | |
|----------------|-----|--|
| PIN SIGNAL | | |
| 1 | GND | |
| 2 | GND | |
| 3 | 12V | |
| 4 | 12V | |
| | | |



- Make sure that all the connectors are connected to proper ATX power supplies to ensure stable operation of the mainboard.
- Power supply of 130 watts (and above) is highly recommended for system stability.
- 3. ATX 12V power connection should be greater than 6A.

Back Panel



Mouse/Keyboard

The standard PS/2 $^{\circ}$ mouse/keyboard DIN connector is for a PS/2 $^{\circ}$ mouse/keyboard.

DVI Port

The DVI (Digital Visual Interface) connector allows you to connect an LCD monitor. It provides a high-speed digital interconnection between the computer and its display device. To connect a LCD monitor, simply plug your monitor cable into the DVI connector, and make sure that the other end of the cable is properly connected to your monitor (refer to your monitor manual for more information).

VGA Port

The DB15-pin female connector is provided for monitor.

Serial Port

The serial port is a 16550A high speed communications port that sends/ receives 16 bytes FIFOs. You can attach a serial mouse or other serial devices directly to the connector.

USB Port

The USB (Universal Serial Bus) port is for attaching USB devices such as keyboard, mouse, or other USB-compatible devices.

► LAN

The standard RJ-45 LAN jack is for connection to the Local Area Network (LAN). You can connect a network cable to it.



| LED | Color | LED State | Condition | |
|--------------------|--------|-------------------------|---|--|
| | | Off | LAN link is not established. | |
| Left | Orange | On (steady state) | LAN link is established. | |
| On (brighter & pul | | On (brighter & pulsing) | The computer is communicating with another computer on the LAN. | |
| | Green | Off | 10 Mbit/sec data rate is selected. | |
| | | On | 100 Mbit/sec data rate is selected. | |
| | Orange | On | 1000 Mbit/sec data rate is selected. | |

Audio Port Connectors

These audio connectors are used for audio devices. You can differentiate the color of the audio jacks for different audio sound effects.

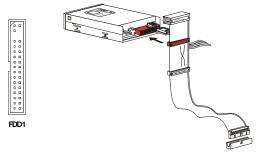
 Line-In (Blue) - Line In / Side-Surround Out in 5.1 channel mode, is used for external CD player, tapeplayer or other audio devices.
 Line-Out (Green) - Line Out, is a connector for speakers or headphones.

Mic (Pink) - Mic, is a connector for microphones.

Connectors

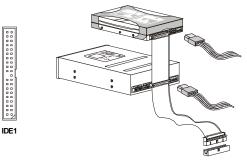
Floppy Disk Drive Connector: FDD1

This connector supports 360KB, 720KB, 1.2MB, 1.44MB or 2.88MB floppy disk drive.



IDE Connector: IDE1

This connector supports IDE hard disk drives, optical disk drives and other IDE devices.

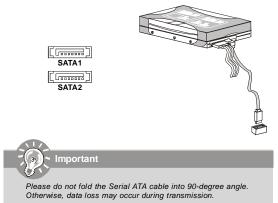




If you install two IDE devices on the same cable, you must configure the drives separately to master / slave mode by setting jumpers. Refer to IDE device's documentation supplied by the vendors for jumper setting instructions.

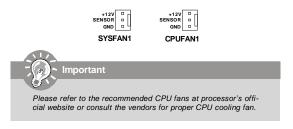
Serial ATAII Connector: SATA1, SATA2

This connector is a high-speed Serial ATA interface port. Each connector can connect to one Serial ATA device.



Fan Power Connectors: SYSFAN1, CPUFAN1

The fan power connectors support system cooling fan with +12V. When connecting the wire to the connectors, always note that the red wire is the positive and should be connected to the +12V; the black wire is Ground and should be connected to GND. If the mainboard has a System Hardware Monitor chipset on-board, you must use a specially designed fan with speed sensor to take advantage of the CPU fan control.



Aux Line-In Connector: TVIN1

This connector is provided for external audio input.



Front Panel Connector: JFP1

The mainboard provides one front panel connector for you to connect to the front panel switches and LEDs. JFP1 is compliant with Intel® Front Panel I/O Connectivity Design Guide.

| | Pin Demition | | |
|--------------|--------------|-----------|---|
| Power Power | PIN | SIGNAL | DESCRIPTION |
| LED Switch | 1 | HD_LED_P | Hard disk LED pull-up |
| 占 浩 | 2 | FPPWR/SLP | MSG LED pull-up |
| 2 (古古古古) 10 | 3 | HD_LED_N | Hard disk active LED |
| 1 (민무(무)면) 9 | 4 | FPPWR/SLP | MSG LED pull-up |
| \FF | 5 | RST_SW_N | Reset Switch low reference pull-down to GND |
| HDD Reset | 6 | PWR_SW_P | Power Switch high reference pull-up |
| LED Switch | 7 | RST_SW_P | Reset Switch high reference pull-up |
| JFP1 | 8 | PWR_SW_N | Power Switch low reference pull-down to GND |
| | 9 | RSVD_DNU | Reserved. Do not use. |
| | | | |

Front Panel Audio Connector: JAUD1

This connector allows you to connect the front panel audio and is compliant with Intel® Front Panel I/O Connectivity Design Guide.

| | Pin Definition | | | | |
|--------------|----------------|-------------|--|--|--|
| | PIN | SIGNAL | DESCRIPTION | | |
| | 1 | N/C | N/C | | |
| | 2 | AUD_GND | Ground used by analog audio circuits | | |
| 9 00000 1 | 3 | AUD_MIC | Microphonepower | | |
| 10 0 0 0 0 2 | 4 | AUD_VCC | Filtered +5V used by analog audio circuits | | |
| 00000 | 5 | AUD_FPOUT_R | Right channel audio signal to front panel | | |
| JAUD1 | 6 | AUD_RET_R | Right channel audio signal return from front panel | | |
| | 7 | N/C | N/C | | |
| | 8 | KEY | Nopin | | |
| | 9 | AUD_FPOUT_L | Left channel audio signal to front panel | | |
| | 10 | AUD_RET_L | Left channel audio signal return from front panel | | |

) Important

If you don't want to connect to the front audio header, pins 5 & 6, 9 & 10 have to be jumpered in order to have signal output directed to the rear audio ports. Otherwise, the Line-Out connector on the back panel will not function.

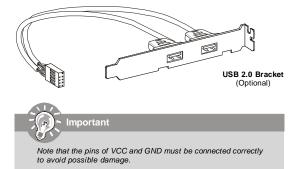


Front USB Connector: F_USB1, F_USB2

This connector, compliant with Intel® I/O Connectivity Design Guide, is ideal for connecting high-speed USB interface peripherals such as USB HDD, digital cameras, MP3 players, printers, modems and the like.



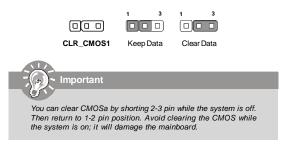
| Pin Definition | | | | | | |
|----------------|----------------|----|-------|--|--|--|
| PIN | PIN SIGNAL PIN | | | | | |
| 1 | VCC | 2 | VCC | | | |
| 3 | USB0- | 4 | USB1- | | | |
| 5 | USB0+ | 6 | USB1+ | | | |
| 7 | GND | 8 | GND | | | |
| 9 | Key (no pin) | 10 | USBOC | | | |



Jumper

Clear CMOS Jumper: CLR_CMOS1

There is a CMOS RAM onboard that has a power supply from an external battery to keep the data of system configuration. With the CMOS RAM, the system can automatically boot OS every time it is turned on. If you want to clear the system configuration, set the jumper to clear data.



Slot

PCI (Peripheral Component Interconnect) Slot

The PCI slot supports LAN card, SCSI card, USB card, and other add-on cards that comply with PCI specifications.

| | 00000 |
|---|-------|
| | |
| <u>[000000000000000000000000000000000000</u> | 00000 |

32-bit PCI Slot

PCI Interrupt Request Routing

The IRQ, acronym of interrupt request line and pronounced I-R-Q, are hardware lines over which devices can send interrupt signals to the microprocessor. The PCI IRQ pins are typically connected to the PCI bus pins as follows:

| | Order 1 | Order 2 | Order 3 | Order 4 |
|-------------|---------|---------|---------|---------|
| 32-bit PCI1 | INT A# | INT B# | INT C# | INTD# |



When adding or removing expansion cards, make sure that you unplug the power supply first. Meanwhile, read the documentation for the expansion card to configure any necessary hardware or software settings for the expansion card, such as jumpers, switches or BIOS configuration.

Chapter 3 BIOS Setup

This chapter provides the information on the BIOS Setup program and allows you to configure the system for optimum use.

You may need to run the Setup program when: ► An error message appears on the screen during the system booting up, and requests you to run SETUP. ► You want to change the default settings for customized features.



MS-7265 Mainboard

Entering Setup

Power on the computer and the system will start POST (Power On Self Test) process. When the message below appears on the screen, press key to enter Setup.

Press DEL to enter SETUP

If the message disappears before you respond and you still wish to enter Setup, restart the system by turning it OFF and On or pressing the RESET button. You may also restart the system by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys.



- The items under each BIOS category described in this chapter are under continuous update for better system performance. Therefore, the description may be slightly different from the latest BIOS and should be held for reference only.
- Upon boot-up, the 1st line appearing after the memory count is the BIOS version. It is usually in the format:

W7265IIP V3.0 070629 where:

▶ 1st digit refers to BIOS maker as A= AMI, W= AWARD, and P= PHOENIX.

2nd - 5th digit refers to the model number.

6th digit refers to the chipset as I= Intel, N= nVidia, and V= VIA.

- Tth 8th digit refers to the product type as IP= IPC.
- V3.0 refers to the BIOS version.
- ▶ 070629 refers to the date this BIOS was released.

Control Keys

| <^> | Move to the previous item |
|-----------------|---|
| <↓> | Move to the next item |
| <←> | Move to the item in the left hand |
| <→> | Move to the item in the right hand |
| <enter></enter> | Select the item |
| <esc></esc> | Jumps to the Exit menu or returns to the main menu from a submenu |
| <+/PU> | Increase the numeric value or make changes |
| <-/PD> | Decrease the numeric value or make changes |
| <f1></f1> | General Help |
| <f5></f5> | Previous Values |
| <f6></f6> | Fail-Safe Defaults |
| <f7></f7> | Optimized Defaults |
| <f10></f10> | Save & Exit Setup |
| | |

Getting Help

After entering the Setup menu, the first menu you will see is the Main Menu.

Main Menu

The main menu lists the setup functions you can make changes to. You can use the arrow keys $(\uparrow\downarrow)$ to select the item. The on-line description of the highlighted setup function is displayed at the bottom of the screen.

Sub-Menu

If you find a right pointer symbol (as shown in the right view) appears to the left of certain fields that means a sub-

| | | Channe 1 | | | 1 | None] |
|----|-----|----------|---|-------|----|----------------|
| | | Channel | | | | |
| | | Channe 1 | | | Ļ | None] None] |
| ٠. | IDF | Channel | 1 | Slave | L. | Nonej |

menu can be launched from this field. A sub-menu contains additional options for a field parameter. You can use arrow keys ($\uparrow\downarrow$) to highlight the field and press <Enter> to call up the sub-menu. Then you can use the control keys to enter values and move from field to field within a sub-menu. If you want to return to the main menu, just press the <Esc >.

General Help <F1>

The BIOS setup program provides a General Help screen. You can call up this screen from any menu by simply pressing <F1>. The Help screen lists the appropriate keys to use and the possible selections for the highlighted item. Press <Esc> to exit the Help screen.

The Main Menu

| Advanced Chipset Features Integrated Peripherals | Load Optimized Defaults Set Supervisor Passuord |
|--|--|
| ▶ Power Management Setup ▶ PnP/PCI Configurations | Set User Password Save & Exit Setup |
| | Exit Without Saving |

Standard CMOS Features

Use this menu for basic system configurations, such as time, date etc.

Advanced BIOS Features

Use this menu to setup the items of the special enhanced features.

Advanced Chipset Features

Use this menu to change the values in the chipset registers and optimize your system's performance.

► Integrated Peripherals

Use this menu to specify your settings for integrated peripherals.

Power Management Setup

Use this menu to specify your settings for power management.

PnP/PCI Configurations

This entry appears if your system supports PnP/PCI.

▶ PC Health Status

This entry shows your PC health status.

Frequency/ Voltage Control

Use this menu to specify your settings for frequency/ voltage control.

Load Fail-Safe Defaults

Use this menu to load the default values set by the BIOS vendor for stable system performance.

Load Optimized Defaults

Use this menu to load the default values set by the mainboard manufacturer specifically for optimal performance of the mainboard.

► Set Supervisor Password Use this menu to set Supervisor Password.

Set User Password Use this menu to set User Password.

Save & Exit Setup Save changes to CMOS and exit setup.

Exit Without Saving

Abandon all changes and exit setup.

Standard CMOS Features

| Date (nn:dd:yy) Tine (hh:nn:ss) | Jul 4 2006 | Iten Help |
|--|--|-----------|
| ► IDE Channel 8 Master | [None] [HDC HD400EB-11CPF0] [None] | |
| Drive A Halt On System Information | [None] [All Errors] [Press Enter] | |
| | | |

Date (mm:dd:yy)

This allows you to set the system to the date that you want (usually the current date). The format is <day> <month> <date> <year>.

- Day of the week, from Sun to Sat, determined by BIOS. Read only.
- Month The month from Jan. through Dec.
- Date The date from 1 to 31 can be keyed by numeric function keys.
- Year The year can be adjusted by users.

Time (hh:mm:ss)

This allows you to set the system time that you want (usually the current time). The time format is <hour> <minute> <second>.

▶ IDE Channel 0/1/2/3 Master/Slave

Press <+> or <-> to select the hard disk drive type. The specification of hard disk drive will show up on the right hand according to your selection. Press <Enters for the sub-menu of each item:

| IDE HDD Auto-Detection | [Press Enter] | Iten Help |
|-------------------------------------|------------------|---|
| IDE Channel 0 Master Access Mode | [Auto] [Auto] | Menu Level 🕨 |
| | 0 MB | To auto-detect the HDD's size, head of this channel |
| Culinder | | |
| Head | | |
| | | |
| Landing Zone | | |
| Sector | | |

IDE HDD Auto-Detection

Press Enter to allow BIOS to auto-detect the type of the HDDs.

► IDE Channel 0 Master

Press PgUp/<+> or PgDn/<-> to select Manual, None or Auto type. Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category. If your hard disk drive type is not matched or listed, you can use Manual to define your own drive type manually.

If you select *Manual*, related information is asked to be entered to the following items. Enter the information directly from the keyboard. This information should be provided in the documentation from your hard disk vendor or the system manufacturer.

| Access Mode | The settings are CHS, LBA, Large, Auto. |
|--------------|---|
| Capacity | The formatted size of the storage device. |
| Cylinder | Number of cylinders. |
| Head | Number of heads. |
| Precomp | Write precompensation. |
| Landing Zone | Cylinder location of the landing zone. |
| Sector | Number of sectors. |

System Information

Press <Enter> to for the sub-menu of each item:

| | Phoenix - RwardBlUS CMUS Setup System Information | Utility |
|---|--|---------------|
| BIOS version CPU Tupe | 945 Platinum W72651MS V8.80 | Iten Help |
| CPU ID/uCodelD CPU Frequency CPU L2 Cache | | Henu Level →> |

BIOS Version

This item shows the BIOS version of your system (read only).

CPU Type/ CPU ID/ uCode ID/ CPU Frequency/ CPU L2 Cache

The three items show the CPU related information of your system (read only).

Advanced BIOS Features

| ▶ Hard Disk Boot Priority [Press Enter] | Iten Help |
|---|--------------------------|
| Virus Harning (Disabled) CPU L3 Cache (Enabled) First Boot Device (Removable) Second Boot Device (CBBOH) Third Boot Device (Hard Disk) Boot Other Device (Enabled) Boot Up MenLock Status (On) Security Option (Securp) APIC Mode (Enabled) HPIC Work (Securp) PIC (Securp) | |
| ↓++:Nove Enter:Select +/-/PU/PD:Value F10:Sa | ve ESC:Exit F1:General H |

Hard Disk Boot Priority

Press [Enter] to enter a sub menu which shows every current hard drive installed. Use [PageUp] or [PageDown] key to select the first boot hard disk.



Virus Warning

The item is to set the Virus Warning feature for IDE Hard Disk boot sector protection. If the function is enabled and any attempt to write data into this area is made, BIOS will display a warning message on screen and beep.

CPU L3 Cache

Level 3 cache is the extra cache built into motherboards between the microprocessor and the main memory. Located away from the CPU, the L3 cache is slower than the L1 & L2 caches. This setting allows you to turn on or off the L3 cache.

First Boot Device/ Second Boot Device/ Third Boot Device

The items allow you to set the sequence of boot devices where BIOS attempts to load the disk operating system.

Boot Other Device

Setting the option to [Enabled] allows the system to try to boot from other device if the system fails to boot from the 1st/2nd/3rd boot device.



Boot Up NumLock Status

This setting is to set the Num Lock status when the system is powered on. Setting to [On] will turn on the Num Lock key when the system is powered on. Setting to [Off] will allow users to use the arrow keys on the numeric keypad. Security Option

This specifies the type of BIOS password protection that is implemented. Settings are described below:

Security Option

This specifies the type of BIOS password protection that is implemented. Settings are described below:

- Setup The password prompt appears only when end users try to run Setup.
- System A password prompt appears every time when the computer is powered on or when end users try to run Setup.

► APIC Mode

This field is used to enable or disable the APIC (Advanced Programmable Interrupt Controller). Due to compliance with PC2001 design guide, the system is able to run in APIC mode. Enabling APIC mode will expand available IRQ resources for the system.

MPS Version Control For OS

This field allows you to select which MPS (Multi-Processor Specification) version to be used for the operating system. You need to select the MPS version supported by your operating system. To find out which version to use, consult the vendor of your operating system.

Advanced Chipset Features



On-Chip Frame Buffer Size

Frame Buffer is the video memory that stores data for video display (frame). This field is used to determine the memory size for Frame Buffer. Larger frame buffer size increases video performance.

DVMT Mode

Use the field to select the mode of the digital monitor you use. Setting options: [Fixed Mode] [DVMT Mode] [Both].

DVMT/ FIXED Memory Size

Specify the size of DVMT memory to allocate for video memory.



Integrated Peripherals

| OnChip IDE Device | (Press Enter) [Press Enter] | Iten Help |
|-------------------|--------------------------------|--------------|
| | [Press Enter] | Menu Level → |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |

OnChip IDE Device

Press <Enter> and the following sub-menu appears:

| *** On-Chip Serial ATA Se SATA Mode | tting *** CTDB1 | Item Help |
|--|--|---|
| On-Chip Serial ATA On-Chip Serial ATA SATA PORT Speed Settings × PHTA IUE Node SATA Port | (Enhanced Mode) (Bisabled) Secondary P0,P2 is Prinary | Menu Level Select IDE if you war to SATA convert to IDE .Select RAID if you want to have RAID functionif motherboar is connocting two SATA HDD at least. |

SATA Mode

This item allows you to configure SATA mode

- [IDE] As serial ATA only.
- [RAID] As SATA RAID mode Supporting RAID0, 1, 5, 10.
- [AHCI] If AHCI is chosen, it allows you to enable SATA Stagger Spinup Support (not RAID mode) and take all hard disks on board as master.

On-Chip Serial ATA

This setting is used to specify the SATA controller. The settings are:

| [Disabled] [Auto] | Disable the SATA controller. PATA and SATA will be arranged by BIOS, and you will be able to see the IDE Device status listed in Standard COMS Features . |
|----------------------|---|
| [Legacy Mode] | PATA and SATA will be combined. Max. of 2 IDE drives in each channel are available. |
| [Native Mode] | PATA and SATA will both be enabled. Max. of 6 IDE drives are supported. |

SATA PORT Speed Settings

This setting controls the speed of the SATA port.

Onboard Device

Press <Enter> and the following sub-menu appears:

| SB Xeyboard Support [Disabled] Henu Level ⊁≀ C97 Audio [Auto] | Regboard Support [Disabled] Henu Level ⊁F 7 Audio [Auto] | ISB Controller ISB 2.0 Controller | [Enabled] [Enabled] | Item Help |
|--|---|--------------------------------------|------------------------|---------------|
| nhoard LAN Device 1 [Fnabled] | oard LAN Device 2 [Enabled] | SB Keyboard Support | [Disabled] | Henu Level ⊁⊧ |
| | | board LAN Device 1 | | |

USB Controller

This setting is used to enable/disable the onboard USB controller.

USB 2.0 Controller

This setting is used to enable/disable the onboard USB 2.0 controller.

USB Keyboard Support

Select Enabled if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard.

AC97 Audio

[Auto] allows the mainboard to detect whether an audio device is used. If an audio device is detected, the onboard AC97 (Audio Codec'97) controller will be enabled; if not, it is disabled. Disable the controller if you want to use other controller cards to connect an audio device.

Onboard LAN Device 1/2

This setting controls the onboard LAN 1/2 device.

Onboard Lan 1/2 Boot ROM

These items enable or disable the initialization of the onboard LAN Boot ROMs during bootup. Selecting [Disabled] will speed up the boot process.

SuperIO Device

Press <Enter> and the following sub-menu appears:

| Phoenix - AwardBIOS CHOS Setup Utility SuperIO Device | | |
|--|-----------|-----------|
| Onboard FDC Controller | [Enabled] | Item Help |
| Onboard Serial Port 1 | | |

Onboard FDC Controller

Select [Enabled] if your system has a floppy disk controller (FDD) installed on the system board and you wish to use it. If you install add-on FDC or the system has no floppy drive, select [Disabled] in this field.

Onboard Serial Port 1/2

These items specify the base I/O port addresses of the onboard Serial Port 1/ Serial Port 2. Selecting [Auto] allows AMIBIOS to automatically determine the correct base I/O port address. Settings: [3F8/IRQ4], [2F8/ IRQ3], [3E8/IRQ4], [2E8/IRQ3] and [Disabled].

Watch Dog Timer

You can enable the system watch-dog timer, a hardware timer that generates either an NMI or a reset when the software that it monitors does not respond as expected each time the watch dog polls it (select the time period in a separate field). See the WDT fields, below.

Power Management Setup



ACPI Suspend Type

This item specifies the power saving modes for ACPI function. If your operating system supports ACPI, such as Windows 98SE, Windows ME, Windows 2000 and Windows XP, you can choose to enter the Standby mode in S1 (POS) or S3 (STR) fashion through the setting of this field. Options are:

- [S1/POS] The S1 sleep mode is a low power state. In this state, no system context is lost (CPU or chipset) and hardware main tains all system context.
- [S3/STR] The S3 sleep mode is a lower power state where the infor mation of system configuration and open applications/files is saved to main memory that remains powered while most other hardware components turn off to save energy. The information stored in memory will be used to restore the system when a "wake up" event occurs.

Soft-Off by PWR-BTTN

When [Enabled], turning the system off with the on/off button places the system in a very low-power-usage state, with only enough circuitry receiving power to detect power button activity or Resume by Ring activity.

Wake-Up By PCI card

When setting to [Enabled], this setting allows your system to be awakened from the power saving modes through any event on PCI PME (Power Management Event).

USB KB WakeUp From S3 (S4)

When setting to [Enabled], this setting allows your system to be awakened from S3 state.

Resume by Alarm

When [Enabled], your can set the date and time at which the RTC (real-time clock) alarm awakens the system from suspend mode.

POWER ON Function

This controls how the PS/2 mouse or keyboard can power on the system.

PWRON After PWR-fail

This item specifies whether your system will reboot after a power failure or interrupt occurs. Available settings are:

- [Off] Leaves the computer in the power off state.
- [On] Leaves the computer in the power on state.

[Former-sts] Restores the system to the status before power fail ure or interrupt occurred.

BIOS Setup

PNP/ PCI Configurations

| Init Display First | Iten Help |
|--------------------|-----------|
| | |
| | |
| | |
| | |
| | |

Init Display First

This setting specifies which VGA card is your primary graphics adapter. Setting options are:

[PCI Slot] The system initializes the PCI graphic card first.

[Onboard] The system initializes the VGA graphic card first.

PC Health Status

| 12 (U) 11.820 of Shutdoun, the 1.5 (U) 1.580 System uill Shutdoun 5 (U) 5.860 when CPU rise to the DBR2(U) 1.840 temperature that you | | Item Help |
|---|-------------------------|---|
| 5 (0) 5.860 ubm CPU rise to the DBE2(0) 1.840 temperature that you succion 3.310 solet, This Fraction 0.861 (0) 3.810 solet, This Fraction solet, This Fraction solet, This S | | Select the tenperature of Shutdown, the |
| 3USB(U) 3.26U your computer | 5.86V 1.84U 3.31U | when CPU rise to the temperature that you select .This function |
| | | |
| | | |

Current System/CPU Temperature, System/CPU Fan Speed, Vcore, 12(V), 1.5(V), 5(V), DDR2(V), 3VCC(V), VBAT(V), 3VSB(V)

These items display the current status of all of the monitored hardware devices/ components such as CPU voltages, temperatures and all fans' speeds.

Shutdown Temperature

If the CPU temperature reaches the limit preset in this setting, the system will shut down automatically.

Frequency/ Voltage Control

| Auto Detect PCI Clk Spread Spectrum | [Enabled] [Disabled] | Iten Help |
|--|---|-----------|
| operium | | |
| :Move Enter:Select + 5: Previous Values | ✓-✓PU✓PD:Value F18: F6: Fail-Safe Defaul | |

Auto Detect PCI Clk

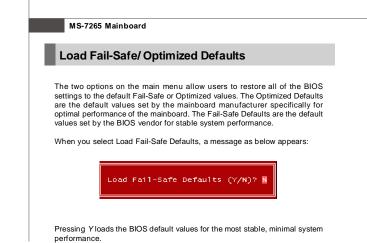
This item is used to auto detect the PCI slots. When set to [Enabled], the system will remove (turn off) clocks from empty PCI slots to minimize the electromagnetic interference (EMI).

Spread Spectrum

When the motherboard's clock generator pulses, the extreme values (spikes) of the pulses creates EMI (Electromagnetic Interference). The Spread Spectrum function reduces the EMI generated by modulating the pulses so that the spikes of the pulses are reduced to flatter curves.



- If you do not have any EMI problem, leave the setting at [Disabled] for optimal system stability and performance. But if you are plagued by EMI, select the value of Spread Spectrum for EMI reduction.
- The greater the Spread Spectrum value is, the greater the EMI is reduced, and the system will become less stable. For the most suitable Spread Spectrum value, please consult your local EMI regulation.
- Remember to disable Spread Spectrum if you are overclocking because even a slight jitter can introduce a temporary boost in clock speed which may just cause your overclocked processor to lock up.





Set Supervisor/ User Password

When you select Load Optimized Defaults, a message as below appears:



Type the password, up to six characters in length, and press <Enter>. The password typed now will replace any previously set password from CMOS memory. You will be prompted to confirm the password. Retype the password and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To clear a set password, just press <Enter> when you are prompted to enter the password. A message will show up confirming the password will be disabled. Once the password is disabled, the system will boot and you can enter Setup without entering any password.

When a password has been set, you will be prompted to enter it every time you try to enter Setup. This prevents an unauthorized person from changing any part of your system configuration.

Chapter 4 System Resources

This chapter provides information on the following system resources:

- 1. Watch Dog Timer Setting (p.4-2);
- 2. Award POST Code (p.4-5);
- 3. PCI Configuration (p.4-11);
- 4. Resource List (p.4-12).



Watchdog Timer Setting

This watchdog timer is using Super I/O Winbond W83627EHG pin 77 WDTO# pin to system reset.

Setup Procedure

1. Enter super I/O configuration mode

| mov | dx, 02eh |
|-----|----------|
| mov | al, 087h |
| out | dx, al |
| out | dx, al |

2. Set pin 77 to WDTO# function

| BIT | READ / WRITE | DESCRIPTION |
|------------------------|--------------------|---|
| 1 | R/W | Pin 75 Select (reset by RSMRST#) = 0 RSMRST# = 1 GPIO51 |
| 0 | R/W | Pin 77 Select (reset by RSMRST#) = 0 WDTO# = 1 GPIO50 |
| ma ma out inc | ov al,0. t dx,a | |

| in | al,dx | |
|-----|---------|-------------------------|
| and | al,0FEh | ;Config Bit 0 As 0 |
| out | dx,al | ;Config PIN 77 as WDTO# |

3. Select Logical Device 8

| . Select Lug | ical Device o | |
|--------------|---------------|---|
| mov | dx, 02eh | |
| mov | al, 07h | |
| out | dx, al | point to Logical Device Number Register |
| inc | dx | |
| mov | al, 08h | select Logical Device 8 |
| out | dx, al | |

4. Enable watchdog timer ; Activate WDTO#

CR 30h. (Default 00h)

| Bit | Read / Write | | Description | |
|-----|--------------|-----------------------|--------------------|--|
| 7~1 | Reserved. | | 2000 | |
| 0 | R/W | 0: WDTO# is inactive. | 1: Activate WDTO#. | |

System Resources

movdx, 02ehmoval, 030houtdx, alincdxmoval, 01houtdx, al

;CR 30h: bit 0 fill in 1

; Setup WDTO# count mode

; Set bit4 and bit 3 by request

; Set bit 2, bit 1 to 0

CR F5h. (WDTO#, PLED and KBC P20 control mode register; Default 00h)

| BIT | READ / WRITE | DESCRIPTION |
|-----|--------------|--|
| 7~6 | R/W | Select Power LED mode. 00: Power LED pin is th-stated. 01: Power LED pin is driven low. 10: Power LED pin outputs 'Hz pulse with 50% duty cycle. 11: Power LED pin outputs 'LAt pulse with 50% duty cycle. |
| 5 | Reserved. | |
| 4 | R/W | Faster 1000 times for WDTC⊭ count mode. 0. Disable. 1: Enable. (If bit-3 is Second Mode, the count mode be 1/1000 Sec.) (If bit-3 is Minute Mode, the count mode be 1/1000 Min.) |
| 3 | R/W | Select WDTO# count mode. 0: Second Mode. 1: Minute Mode. |
| 2 | R/W | Enable the rising edge of KBC reset (P20) to issue time-out event. 0: Disable. 1: Enable. |
| 1 | R/W | Disable / Enable the WDTO# output low pulse to the KDRST# pin (PIN60) 0: Disable. 1: Enable. |
| 0 | Reserved. | |

CR F7h. (WDTO# control & status register; Default 00h)

| BIT | READ / WRITE | DESCRIPTION |
|-----|--------------------------|--|
| 7 | R/W | Mouse interrupt reset watch-dog timer enable 0: Watchdog timer is not affected by mouse interrupt. 1: Watchdog timer is reset by mouse interrupt. |
| 6 | R/W | Keyboard interrupt reset watch-dog timer enable 0: Watchdog timer is not affected by keyboard interrupt. 1: Watchdog timer is reset by keyboard interrupt. |
| 5 | Write "1" Only | Trigger WDTO# event. This bit is self-clearing. |
| 4 | R / W Write "0" Clear | WDTO# status bit 0: Watchdog timer is running. 1: Watchdog timer issues time-out event. |
| 3~0 | R/W | These bits select IRQ resource for WDTO#. (02h for SMI# event.) |

dx, 02eh mov ;CR F7h: bit 4 fill 0 (clear event) al, 0f7h mov dx, al out inc dx in al,dx and al, 0efh out dx, al ;CR F6h: bit0~7 fill in counter time

CR F6h. (WDTO# counter register; Default 00h)

MS-7265 Mainboard

| BIT | READ / WRITE | DESCRIPTION | |
|-----|--------------|---|--|
| 7~0 | R/W | Watch Dog Timer Time-out value. Writing a non-zero value to this register causes the counter to load the value to Watch Dog Counter and start counting down. If the bit 7 and 0 of CR F7h are set, any Mouse Interrupt of Keyboard Interrupt event will also cause the reload of previousy-loaded non-zero value to Watch Dog Counter and start counting down. Reading this register returns current value in Watch Dog Counter instead of Watch Dog Timer Time-out value. 00h: Time-out Disable 01h: Time-out occurs after 1 second/minutes 02h: Time-out occurs after 1 second/minutes 03h: Time-out occurs after 2 second/minutes FFh: Time-out occurs after 255 second/minutes | |

5. Exit configuration mode

| mov | dx, 02eh |
|-----|----------|
| mov | al, 0aah |
| out | dx, al |

Award POST Code

Award BIOS Error Message and Check Point (POST code) List (Need to be modified, TBD)

Short Name Description Possible FRUS CMOS Checksum of CMOS is incorrect, so the System board checksum error system loads the default equipment Defaults configuration. A checksum error may loaded indicate that CMOS has become corrupt. This error may have been caused by a weak battery. Check the battery and replace if necessary. 2 CPU at nnnn Displays the running speed of the CPU. processor Press ESC to The user may press Esc to skip the full System board skip memory memory test. test Floppy disk(s) Cannot find or initialize the floppy drive system board fail controller or the drive. Make sure the controller is installed correctly. If no floppy drives are installed, be sure the Diskette Drive selection in Setup is set to NONE or AUTO. HARD DISK Some hard drives require extra time to System board initializing initialize. Please wait a moment 6 HARD DISK Cannot find or initialize the hard drive controller System board INSTALL or the drive. Make sure the controller is FAILURE installed correctly. If no hard drives are installed, be sure the Hard Drive selection in Setup is set to NONE. Keyboard error Cannot initialize the keyboard. Make sure System board or no kevboard the keyboard is attached correctly and no present kevs are pressed during POST. To purposely configure the system without a keyboard, set the error halt condition in Setup to HALT ON ALL, BUT KEYBOARD. The BIOS then ignores the missing

keyboard during POST.

areas being tested.

This message displays during a full

memory test, counting down the memory

• Error/Process Message.

Memory Test:

8

DIMM

System board

Check Point List

| POST (hex) | Description |
|------------|---|
| CFh | Test CMOS R/W functionality. |
| C0h | Early chipset initialization: |
| | -Disable shadow RAM |
| | -Disable L2 cache (socket 7 or below) |
| | -Program basic chipset registers |
| C1h | Detect memory |
| | Auto-detection of DRAM size, type and ECC. |
| | Auto-detection of L2 cache (socket 7 or below) |
| A1h | Set Initial Conditions (Default Values) in EBP |
| A2h | Determine FSB frequency. |
| A3h | Begin Detection of installed DIMMS |
| A4h | Check for Column Latency |
| A5h | 200Mhz or 266Mhz |
| A6h | Check for tRAS timing |
| A7h | Check for tRP timing |
| A8h | Check for tRCD timing |
| A9h | Check for ECC Support |
| AAh | Check for refresh timing |
| ABh | Verify that the DIMM's are in matched pairs |
| C3h | Expand compressed BIOS code to DRAM |
| C5h | Call chipset hook to copy BIOS back to E000 & F000 shadow |
| | RAM. |
| 01h | Expand the Xgroup codes locating in physical address 1000:0 |
| 02h | Reserved |
| 03h | Initial Superio_Early_Init switch. |
| 04h | Reserved |
| 05h | 1. Blank out screen |
| | 2. Clear CMOS error flag |
| 06h | Reserved |
| 07h | 1. Clear 8042 interface |
| | 2. Initialize 8042 self-test |
| 08h | 1. Test special keyboard controller for Winbond 977 series Super |
| | I/O chips. |
| | Enable keyboard interface. |
| 09h | Reserved |
| 0Ah | Disable PS/2 mouse interface (optional). |
| | 2. Auto detect ports for keyboard & mouse followed by a port & interface swap |
| | (optional). |
| | Reset keyboard for Winbond 977 series Super I/O chips. |
| 0Bh | Reserved |
| 0Ch | Reserved |
| 0Dh | Reserved |
| 0Eh | Test F000h segment shadow to see whether it is R/W-able or not. If |
| | test fails, keep beeping the speaker. |
| 0Fh | Reserved |

| 10h | Auto detect flash type to load appropriate flash R/W codes into the |
|-----|--|
| | run time area in F000 for ESCD & DMI support. |
| 11h | Reserved |
| 12h | Use walking 1's algorithm to check out interface in CMOS |
| | circuitry. Also set real-time clock power status, and then check for |
| | override. |
| 13h | Reserved |
| 14h | Program chipset default values into chipset. Chipset default |
| | values are MODBINable by OEM customers. |
| 15h | Reserved |
| 16h | Initial Early_Init_Onboard_Generator switch. |
| 17h | Reserved |
| 18h | Detect CPU information including brand, SMI type (Cyrix or |
| | Intel) and CPU level (586 or 686). |
| 19h | Reserved |
| 1Ah | Reserved |
| 1Bh | Initial interrupts vector table. If no special specified, all H/W |
| | interrupts are directed to SPURIOUS_INT_HDLR & S/W |
| | interrupts to SPURIOUS_soft_HDLR. |
| 1Ch | Reserved |
| 1Dh | Initial EARLY_PM_INIT switch. |
| 1Eh | Reserved |
| 1Fh | Load keyboard matrix (notebook platform) |
| 20h | Reserved |
| 21h | HPM initialization (notebook platform) |
| 22h | Reserved |
| 23h | Check validity of RTC value: |
| | e.g. a value of 5Ah is an invalid value for RTC minute. |
| | Load CMOS settings into BIOS stack. If CMOS checksum fails, use default |
| | value instead. |
| | Prepare BIOS resource map for PCI & PnP use. If ESCD is valid, take into |
| | consideration of the ESCD's legacy information. |
| | Onboard clock generator initialization. Disable respective clock resource to empty PCI & DIMM slots. |
| | 5. Early PCI initialization: |
| | -Enumerate PCI bus number |
| | -Assign memory & I/O resource |
| | -Search for a valid VGA device & VGA BIOS, and put it |
| | into C000:0. |
| 24h | Reserved |
| 25h | Reserved |
| 26h | Reserved |
| 27h | Initialize INT 09 buffer |
| 28h | Reserved |
| 29h | Program CPU internal MTRR (P6 & PII) for 0-640K memory address. |
| 1 | Initialize the APIC for Pentium class CPU. |
| 1 | Program early chipset according to CMOS setup. Example: onboard IDE |
| 1 | controller. |
| | Measure CPU speed. |
| 1 | 5. Invoke video BIOS. |

MS-7265 Mainboard

| 2Ah | Reserved |
|-----|--|
| 2Bh | Reserved |
| 2Ch | Reserved |
| 2Dh | 1. Initialize multi-language |
| | 2. Put information on screen display, including Award title, CPU type, CPU speed |
| | |
| 2Eh | Reserved |
| 2Fh | Reserved |
| 30h | Reserved |
| 31h | Reserved |
| 32h | Reserved |
| 33h | Reset keyboard except Winbond 977 series Super I/O chips. |
| 34h | Reserved |
| 35h | Reserved |
| 36h | Reserved |
| 37h | Reserved |
| 38h | Reserved |
| 39h | Reserved |
| 3Ah | Reserved |
| 3Bh | Reserved |
| 3Ch | Test 8254 |
| 3Dh | Reserved |
| 3Eh | Test 8259 interrupt mask bits for channel 1. |
| 3Fh | Reserved |
| 40h | Test 8259 interrupt mask bits for channel 2. |
| 41h | Reserved |
| 42h | Reserved |
| 43h | Test 8259 functionality. |
| 44h | Reserved |
| 45h | Reserved |
| 46h | Reserved |
| 47h | Initialize EISA slot |
| 48h | Reserved |
| 49h | Calculate total memory by testing the last double word of each 64K page. |
| | Program write allocation for AMD K5 CPU. |
| 4Ah | Reserved |
| 4Bh | Reserved |
| 4Ch | Reserved |
| 4Dh | Reserved |
| 4Eh | 1. Program MTRR of M1 CPU |
| | 2. Initialize L2 cache for P6 class CPU & program CPU with proper cacheable |
| | range. |
| | Initialize the APIC for P6 class CPU. |
| | 4. On MP platform, adjust the cacheable range to smaller one in case the |
| | cacheable ranges between each CPU are not identical. |
| 4Fh | Reserved |

System Resources

| 50h | Initialize USB |
|-----|--|
| 51h | Reserved |
| 52h | Test all memory (clear all extended memory to 0) |
| 53h | Reserved |
| 54h | Reserved |
| 55h | Display number of processors (multi-processor platform) |
| 56h | Reserved |
| 57h | 1. Display PnP logo |
| | 2. Early ISA PnP initialization |
| | -Assign CSN to every ISA PnP device. |
| 58h | Reserved |
| 59h | Initialize the combined Trend Anti-Virus code. |
| 5Ah | Reserved |
| 5Bh | (Optional Feature) |
| | Show message for entering AWDFLASH.EXE from FDD (optional) |
| 5Ch | Reserved |
| 5Dh | Initialize Init_Onboard_Super_IO switch. |
| | Initialize Init_Onbaord_AUDIO switch. |
| 5Eh | Reserved |
| 5Fh | Reserved |
| 60h | Okay to enter Setup utility; i.e. not until this POST stage can users |
| | enter the CMOS setup utility. |
| 61h | Reserved |
| 62h | Reserved |
| 63h | Reserved |
| 64h | Reserved |
| 65h | Initialize PS/2 Mouse |
| 66h | Reserved |
| 67h | Prepare memory size information for function call: |
| | INT 15h ax=E820h |
| 68h | Reserved |
| 69h | Turn on L2 cache |
| 6Ah | Reserved |
| 6Bh | Program chipset registers according to items described in Setup & |
| | Auto-configuration table. |
| 6Ch | Reserved |
| 6Dh | Assign resources to all ISA PnP devices. |
| | 2. Auto assign ports to onboard COM ports if the corresponding item in Setup |
| | is set to "AUTO". |
| 6Eh | Reserved |
| 6Fh | 1. Initialize floppy controller |
| | Set up floppy related fields in 40:hardware. |
| 70h | Reserved |
| 71h | Reserved |
| 72h | Reserved |
| 73h | (Optional Feature) |
| | Enter AWDFLASH.EXE if : |
| | -AWDFLASH is found in floppy drive. |
| | -ALT+F2 is pressed |
| 74h | Reserved |
| 75h | Detect & install all IDE devices: HDD, LS120, ZIP, CDROM |

MS-7265 Mainboard

| 76h | Reserved |
|-----|--|
| 77h | Detect serial ports & parallel ports. |
| 78h | Reserved |
| 79h | Reserved |
| 7Ah | Detect & install co-processor |
| 7Bh | Reserved |
| 7Ch | Reserved |
| 7Dh | Reserved |
| 7Eh | Reserved |
| 7Fh | Switch back to text mode if full screen logo is supported. |
| | -If errors occur, report errors & wait for keys |
| | -If no errors occur or F1 key is pressed to continue: |
| | wClear EPA or customization logo. |
| 80h | Reserved |
| 81h | Reserved |
| 82h | 1. Call chipset power management hook. |
| - | 2. Recover the text fond used by EPA logo (not for full screen logo) |
| | If password is set, ask for password. |
| 83h | Save all data in stack back to CMOS |
| 84h | Initialize ISA PnP boot devices |
| 85h | 1. USB final Initialization |
| | 2. NET PC: Build SYSID structure |
| | 3. Switch screen back to text mode |
| | Set up ACPI table at top of memory. |
| | 5. Invoke ISA adapter ROMs |
| | 6. Assign IRQs to PCI devices |
| | 7. Initialize APM |
| | 8. Clear noise of IRQs. |
| 86h | Reserved |
| 87h | Reserved |
| 88h | Reserved |
| 89h | Reserved |
| 90h | Reserved |
| 91h | Reserved |
| 92h | Reserved |
| 93h | Read HDD boot sector information for Trend Anti-Virus code |
| 94h | 1. Enable L2 cache |
| 0 | 2. Program boot up speed |
| | 3. Chipset final initialization. |
| | 4. Power management final initialization |
| | 5. Clear screen & display summary table |
| | 6. Program K6 write allocation |
| | 7. Program P6 class write combining |
| 95h | 1. Program daylight saving |
| | 2. Update keyboard LED & typematic rate |
| 96h | 1. Build MP table |
| | 2. Build & update ESCD |
| | 3. Set CMOS century to 20h or 19h |
| | 4. Load CMOS time into DOS timer tick |
| | 5. Build MSIRQ routing table. |
| FFh | Boot attempt (INT 19h) |
| | |

PCI Configuration

PCI Interrupt Request Routing

The IRQ, acronym of interrupt request line and pronounced I-R-Q, are hardware lines over which devices can send interrupt signals to the microprocessor. The PCI IRQ pins are typically connected to the PCI bus pins as follows:

| DEVICE | MCP1 INT Pin | IDSEL | CLOCK | REQ#/GNT# |
|---------------|--------------|-------|----------|-------------|
| PCI Slot | PIRQA | AD17 | PCICLK 0 | REQ#0/GNT#0 |
| Mini PCI Slot | PIRQB | AD18 | PCICLK 1 | REQ#1/GNT#1 |
| LAN1 | PIRQC | AD21 | CLKLAN 1 | REQ#2/GNT#2 |
| LAN2 | PIRQD | AD22 | CLKLAN 2 | REQ#3/GNT#3 |

MS-7265 Mainboard

Resource List

I/O Map

| I/O Port Description 0000-000F DMA Controller 1 0020-0021 Interrupt Controller 1 0040-0043 System Timer 0040-0044 System Timer 0040-0045 System Timer 0040-0047 SIO Port 0040-0048 Keyboard Controller 0040-0049 RTC and CMOS 0070-0073 RTC and CMOS 0080-0084 MA Controller Page Registers 0092 Port 92h 0040-0041 Interrupt Controller 2 0080-0085 APM register 0080-0086 MA Controller 2 00F0-00FF Numeric Data Processor 01F0-01F7 Primary IDE Controller 01F0-01F7 Primary IDE Controller 02F8-02FF COM2 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Primary IDE Controller 0376 SPG 0376 ORH 0376 COM1 0400- | | 1 |
|--|-----------|-------------------------------|
| OD20-0021 Interrupt Controller 1 0040-0043 System Timer 0040-0044 Sto Port 0060-0024 Keyboard Controller 0060-0004 Keyboard Controller 0060-0004 Keyboard Controller 0070-0073 RTC and CMOS 0080-0090 DMA Controller Page Registers 0092 Port 92h 00A0-00A1 Interrupt Controller 2 00B2-00B3 APM register 00C0-00DF DMA Controller 2 00F0-000FF Numeric Data Processor 01F0-01F7 Secondary IDE Controller 01F0-01F7 Primary IDE Controller 02F8-02FF COM4 02F8-03FF COM3 03F6-03FF COM3 03F6 Primary IDE Controller 03F8-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | I/O Port | Description |
| Odd-0.0043 System Timer 0040-0045 SiO Port 0040-0047 SiO Port 0060.0064 Keyboard Controller 0070-0073 RTC and CMOS 0080-00090 DMA Controller Page Registers 0092 Port 92h 0082-0015 DMA Controller 7 0082-0016 Interrupt Controller 2 0082-0017 DMA Controller 2 0070-007F Numeric Data Processor 0170-0177 Secondary IDE Controller 0170-0177 Secondary IDE Controller 02E8-02EF COM4 02F8-02FF COM2 0376 Secondary IDE Controller 0376 Primary IDE Controller 0376 Primary IDE Controller 0376 Primary IDE Controller 0376 | 0000-000F | DMA Controller 1 |
| OddE:004F SIO Port 004E:004F SIO Port 0060,0064 Keyboard Controller 0070-0073 RTC and CMOS 0080-0090 DMA Controller Page Registers 0092 Port 92h 0080-0081 Interrupt Controller 2 0080-0085 APM register 0060-000F DMA Controller 2 00F0-00FF Numeric Data Processor 0170-0177 Secondary IDE Controller 02E8-02EF COM4 02F8-02FF COM4 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Primary ID | 0020-0021 | Interrupt Controller 1 |
| Observe Controller 0060.0064 Keyboard Controller 0070-0073 RTC and CMOS 0080-0090 DMA Controller Page Registers 0092 Port 92h 0040-00011 Interrupt Controller 2 0082-0083 APM register 0060-000F DMA Controller 2 00F0-00FF Numeric Data Processor 01F0-01F7 Primary IDE Controller 02F8-02FF COM4 02F8-02FF COM3 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Primary IDE Controller 0376 Pr | 0040-0043 | System Timer |
| 0070-0073 RTC and CMOS 0080-0090 DMA Controller Page Registers 0092 Port 92h 00A0-00A1 Interrupt Controller 2 00B2-000B3 APM register 000C0-00DF DMA Controller 2 00F0-00DF DMA Controller 2 00F0-00FF Numeric Data Processor 0170-0177 Secondary IDE Controller 01F0-01F7 Primary IDE Controller 02E8-02FF COM2 0376 Secondary IDE Controller 0376 COM3 0376 Primary IDE Controller 0376 Primary IDE Controller 0376 COM3 0376 Primary IDE Controller 0376 Secondary IDE Controller 0376 Primary IDE Controller 0376 <td< td=""><td>004E-004F</td><td>SIO Port</td></td<> | 004E-004F | SIO Port |
| O080-0070 DMA Controller Page Registers 0092 Port 92h 00A0-00A1 Interrupt Controller 2 00B2-00B3 APM register 00E0-00DF DMA Controller 2 00F0-00FF Numeric Data Processor 01F0-01F7 Primary IDE Controller 01F0-01F7 Primary IDE Controller 02E8-02FF COM4 02F8-02FF COM2 0376 Secondary IDE Controller 0376 COM3 0378-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 0060,0064 | Keyboard Controller |
| 0092 Port 92h 00A0-00A1 Interrupt Controller 2 00B2-00B3 APM register 00C0-00DF DMA Controller 2 00F0-00FF Numeric Data Processor 0170-0177 Secondary IDE Controller 01F0-01F7 Primary IDE Controller 02E8-02FF COM4 02F8-02FF COM2 0376 Secondary IDE Controller 0376 Secondary IDE Controller 03F6 OPT1 03F8-03FF COM3 03F8 OSFF 03F8 COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 0070-0073 | RTC and CMOS |
| OA0-00A1 Interrupt Controller 2 00B2-00B3 APM register 00C0-00DF DMA Controller 2 00F0-00FF Numeric Data Processor 0170-0177 Secondary IDE Controller 01F0-0177 Percondary IDE Controller 02F8-02FF COM4 02F8-02FF COM4 0376 Secondary IDE Controller 0376 COM3 0376 COM3 0376 Primary IDE Controller 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 | 0080-0090 | DMA Controller Page Registers |
| O0B2-00B3 APM register 00C0-00DF DMA Controller 2 00F0-00FF Numeric Data Processor 0170-0177 Secondary IDE Controller 01F0-01F7 Primary IDE Controller 02F8-02FF COM4 02F8-02FF COM4 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Primary IDE Controller 0376 Secondary IDE Controller 0376 COM3 0376 Primary IDE Controller 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0400-045F </td <td>0092</td> <td>Port 92h</td> | 0092 | Port 92h |
| OCC-00DF DMA Controller 2 00F0-00FF Numeric Data Processor 0170-0177 Secondary IDE Controller 01F0-01F7 Primary IDE Controller 02E8-02FF COM4 02F8-02FF COM4 0376 Secondary IDE Controller 0378-037F LPT1 038E-03EF COM3 0376 Primary IDE Controller 03F6 Primary IDE Controller 03F6 COM3 03F6 Primary IDE Controller 03F8-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 00A0-00A1 | Interrupt Controller 2 |
| OOFO-OOFF Numeric Data Processor 0170-0177 Secondary IDE Controller 01F0-01F7 Primary IDE Controller 02E8-02EF COM4 02F8-02FF COM2 0376 Secondary IDE Controller 0378-037F LPT1 03F6-03FF COM3 03F6 Primary IDE Controller 03F6 COM3 03F6 Primary IDE Controller 03F8-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 00B2-00B3 | APM register |
| O170-0177 Secondary IDE Controller 0170-0177 Primary IDE Controller 02E8-02EF COM4 02E8-02FF COM2 0376 Secondary IDE Controller 0378-037F LPT1 03E8-03EF COM3 0376 Primary IDE Controller 0376-0 Primary IDE Controller 0376-0 Primary IDE Controller 0376-0 Primary IDE Controller 0378-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 00C0-00DF | DMA Controller 2 |
| OIF0-0IF7 Primary IDE Controller 02E8-02EF COM4 02F8-02FF COM2 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Secondary IDE Controller 0376 Primary IDE Controller 0376 Primary IDE Controller 0376 Primary IDE Controller 0376 COM3 0376 COM3 0376 COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 00F0-00FF | Numeric Data Processor |
| 02E8-02EF COM4 02F8-02FF COM2 0376 Secondary IDE Controller 0378-037F LPT1 03E8-03EF COM3 03F6 Primary IDE Controller 03F6 Primary IDE Controller 03F8-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 0170-0177 | Secondary IDE Controller |
| O2F8-02FF COM2 0376 Secondary IDE Controller 0378-037F LPT1 0378-037F COM3 0376 Primary IDE Controller 0376 Primary IDE Controller 0376 O376 0376 COM3 0376 Primary IDE Controller 038F0-3FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 01F0-01F7 | Primary IDE Controller |
| O376 Secondary IDE Controller 0378-037F LPT1 03E8-03FF COM3 03F6 Primary IDE Controller 03F8-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 02E8-02EF | COM4 |
| 0378-037F LPT1 0328-03FF COM3 03F6 Primary IDE Controller 03F8-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 02F8-02FF | COM2 |
| 03E8-03EF COM3 03F6 Primary IDE Controller 03F8-03FF COM1 0400-045F ACPI I/O space 050-050F SMBus I/O Space | 0376 | Secondary IDE Controller |
| O3F6 Primary IDE Controller 03F8-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 0378-037F | LPT1 |
| 03F8-03FF COM1 0400-045F ACPI I/O space 0500-050F SMBus I/O Space | 03E8-03EF | COM3 |
| O400-045F ACPI I/O space 0500-050F SMBus I/O Space | 03F6 | Primary IDE Controller |
| 0500-050F SMBus I/O Space | 03F8-03FF | COM1 |
| | 0400-045F | ACPI I/O space |
| 0CF8-0CFF PCI configuration Port | 0500-050F | SMBus I/O Space |
| | 0CF8-0CFF | PCI configuration Port |

PCI Devices

| Device | Ven. ID | Dev. ID | BUS# | DEV# | FUNC# |
|----------------------|---------|---------|------|------|-------|
| Intel Bridge | 8086 | 27A0 | 00 | 00 | 00 |
| Intel VGA | 8086 | 27A2 | 00 | 02 | 00 |
| Intel other Display | 8086 | 27A6 | | | |
| Intel USB Controller | 8086 | 27C8 | 00 | 1D | 00 |
| Intel USB Controller | 8086 | 27C9 | 00 | 1D | 01 |
| Intel USB Controller | 8086 | 27CA | 00 | 1D | 02 |
| Intel USB Controller | 8086 | 27CB | 00 | 1D | 03 |
| Intel USB Controller | 8086 | 27CC | 00 | 1D | 07 |
| Intel PCI Bridge | 8086 | 2448 | 00 | 1E | 00 |
| Intel Audio | 8086 | 27DE | 00 | 1E | 02 |
| Intel Bridge | 8086 | 2707 | 00 | 1F | 00 |
| Intel IDE | 8086 | 27DF | 00 | 1F | 01 |
| Intel IDE | 8086 | 27C4 | 00 | 1F | 02 |
| Intel | 8086 | 27DA | 00 | 1F | 03 |
| Intel Ethernet | 8086 | 1076 | 01 | 02 | 00 |
| Intel Ethernet | 8086 | 1076 | 01 | 02 | 09 |

MS-7265 Mainboard

SMBus Resource Allocation

| Device | Address | Description |
|-------------|-----------|---------------------|
| ICS954129BF | 1101 001X | Clock Generator |
| MS-7 | 0101 111X | MSI ACPI Controller |
| DIMM Slot | 1010 0000 | SPD |
| DIMM Slot | 1010 0010 | SPD |

ISA Interrupt Allocation

| IRQ | Description |
|-------|---------------------------|
| IRQ0 | System Timer |
| IRQ1 | Keyboard Controller |
| IRQ2 | Cascade Interrupt |
| IRQ3 | COM2 |
| IRQ4 | COM1 |
| IRQ5 | COM3 |
| IRQ6 | COM4 |
| IRQ7 | LPT1 |
| IRQ8 | RTC |
| IRQ9 | ACPI Controller Interrupt |
| IRQ10 | PCI Device |
| IRQ11 | PCI Device |
| IRQ12 | PS/2 Mouse |
| IRQ13 | Numeric Data Processor |
| IRQ14 | Primary IDE Controller |
| IRQ15 | Secondary IDE Controller |

ISA DMA Channel Allocation

| DMA Channel | Description |
|-------------|---------------------------|
| Channel 0 | Unassigned 8-bit channel |
| Channel 1 | Unassigned 8-bit channel |
| Channel 2 | Unassigned 8-bit channel |
| Channel 3 | Unassigned 8-bit channel |
| Channel 4 | Cascade channel |
| Channel 5 | Unassigned 16-bit channel |
| Channel 6 | Unassigned 16-bit channel |
| Channel 7 | Unassigned 16-bit channel |
| | |

Appendix A Realtek ALC655 Audio

The barebone is equipped with Realtek[®] ALC655 chip, which provides support for 6-channel audio output, including 2 Front, 2 Rear, 1 Center and 1 Subwoofer channel. ALC655 allows the board to attach 4 or 6 speakers for better surround sound effect. The section will tell you how to install and use the 2-/4-/6-channel audio function on the board.



Installing the Audio Driver

You need to install the driver for Realtek ALC655 chip to function properly before you can get access to 2-/4-/6-channel audio operations. Follow the procedures described below to install the drivers for different operating systems.

Installation for Windows 2000/XP

For Windows® 2000, you must install Windows® 2000 Service Pack2 or later before installing the driver.

The following illustrations are based on Windows® XP environment and could look slightly different if you install the drivers in different operating systems.

1. Insert the companion CD into the CD-ROM drive. The setup screen will automatically appear.

- Kright State Manual

 Kright State Manual

 Kright State Manual

 Kright State System Drive

 Realize ACX7 Audo Once

 Browse CO

 Ext
- 2. Click Realtek AC97 Audio Driver.



3. Click Next to install the AC97 Audio software.



here

4. Click Finish to restart the system.



Software Configuration

After installing the audio driver, you are able to use the 2-/4-/6-channel audio feature now. Click the audio icon of from the window tray at the lower-right corner of the screen to activate the **AC97 Audio Configuration**.

Sound Effect

Here you can select a sound effect you like from the Environment list.

| Environment | ilizer Speaker Configuration Sp | eaker Test S/PDIF-Out HRTF Demo General |
|-------------|---|---|
| | Arena | • Edt |
| | Auditorium Concert Hall Cave | |
| Karaoke | Fundo Fundo Cragelio Sultary Cragelio Sultary Silver Canida Aleg Screet Hourtains Duany Piet Luda Pieter Lindar Water | F Add Sign Cornel Equalar |

You may also edit the properties for an environment as you wish by clicking the **Edit** button, then just scroll the bar in the bottom for each property to adjust.

| | Value | | |
|----------------------|-----------|--|--|
| Property Boom | 0 mB | | |
| Boom HE | -698 mB | | |
| Room Roll-off Factor | 0.000 | | |
| Decay Time | 7.240 s | | |
| Decay HF Ratio | 0.330 | | |
| Reflections | -1166 mB | | |
| Reflections Delay | 0.020 s | | |
| Reverb | 16 mB | | |
| Reverb Delay | 0.030 s | | |
| Diffusion | 100.0% | | |
| Density | 100.0% | | |
| HF Reference | 5000.0 Hz | | |

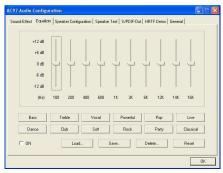
Here it provides the Karaoke function which will automatically remove human voice (lyrics) and leave melody for you to sing the song. Note that this function applies only for 2-channel audio operation.

Just check the $\mbox{Voice Cancellation}$ box and then click \mbox{OK} to activate the Karaoke function.

| Arena | ▼ Edt |
|------------|------------------------------|
| Katooko | Other T Auto Gain Control |
| 40 × Repet | Equalizer |

Equalizer

Here you regulate each equalizer for current playing digital sound sources.



You may choose the provided sound effects, and the equalizer will adjust automatically. If you like, you may also load an equalizer setting or make an new equalizer setting to save as an new one by using the buttons Load and Save. Or you may click Reset to use the default value.

Speaker Configuration

In this tab, you can easily configure your multi-channel audio function and speakers.

- 1. Select a desired multi-channel operation from Number of Speakers.
 - a. N/CHeadphone for the common headphone
 - b. 2-Channel Mode for Stereo-Speaker Output
 - c. 4-Channel Mode for 4-Speaker Output
 - d. 6-Channel Mode for 5.1-Speaker Output
- 2. Then click **OK** to apply the configuration.

| und Effect | Equalizer | Speaker Configuration | Speaker Test | HRTF Demo | Microphone Effect | General |
|---|------------|---------------------------|--------------|------------------|-------------------|-------------|
| No. of Spe | iakers | | | honejack Swite | h | |
| O Headphone | | | 0 | Rear Speaker Out | | |
| O 2 ch | annel mode | for stereo speaker output | e – | | | |
| 4 channel mode for 4 speaker output | | | 0 | Front Speak | er Out | |
| 📀 6 chi | annel mode | for 5.1 speaker output | | | | |
| Synchronize the phonejack switch with the speakers settings | | | ie | 0 | Center/Subwoofer | ipeaker Out |

Speaker Test

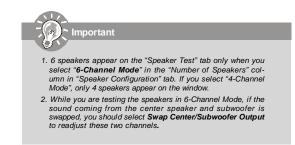
You can use this tab to test each connected speaker to ensure if 4- or 6-channel audio operation works properly. If any speaker fails to make sound, then check whether the cable is inserted firmly to the connector or replace the bad speakers with good ones.



Select this function

Select the speaker by clicking it to test its functionality. The one you select will light up and make testing sound.





HRTF Demo

In this tab you may adjust your HRTF (Head Related Transfer Functions) 3D positional audio before playing 3D audio applications like gaming. You may also select different environment to choose the most suitable environment you like.



General

In this tab it provides some information about the AC97 Audio Configuration utility, including Audio Driver Version, DirectX Version, Audio Controller & AC97 Codec. You may also select the language of this utility by choosing from the Language list.

| and Effect | Equalizer | Speaker Configuration | Speaker Test | HRTF Demo | Microphone Effect | General |
|------------|---------------|-----------------------|--------------------|-----------------|---------------------------|---------|
| Informatio | 'n | | | | | |
| | | Audio Driver Ve | rsion : | 5.10.0.5760 | 1 | |
| | | DirectX Version : | | DirectX 9.0c | | |
| | | Audio Controller : | | rividia(0059) | | |
| | | AC97 C | odec : | ALC655 | | |
| Show is | con in syster | | setting will not b | e activated unt | il you restart this progr | am.) |

Hardware Setup

In addition to a default 2-channel analog audio output function, the audio connectors on the Back Panel also provide 4- or 6-channel analog audio output function if a proper setting is made in the software utility.

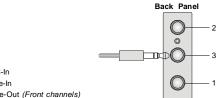
Read the following steps to have the Multi-Channel Audio Function properly set in the software utility, and have your speakers correctly connected to the Back Panel.

Connecting the Speakers

When you have set the Multi-Channel Audio Function mode properly in the software utility, connect your speakers to the correct phone jacks in accordance with the setting in software configuration.

2-Channel Mode for Stereo-Speaker Output

Refer to the following diagram and caption for the function of each phone jack on the back panel when 2-Channel Mode is selected.



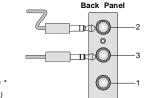
1. MIC-In

- 2 Line-In
- 3. Line-Out (Front channels)

▶ 4-Channel Mode for 4-Speaker Output

The audio jacks on the back panel always provide 2-channel analog audio output function, however these audio jacks can be transformed to 4- or 6- channel analog audio jacks by selecting the corresponding multi-channel operation from **No. of Speakers**.

Refer to the following diagram and caption for the founction of each jack on the back panel when 4-Channel Mode is selected.



1. MIC-In

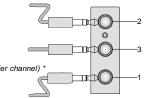
2. Line-Out (Rear channels) *

3. Line-Out (Front channels)

* Line-In function is converted to Line Out function when 4-Channel Mode for 4-Speaker Output is selected.

n 6-Channel Mode for 6-Speaker Output

Refer to the following diagram and caption for the function of each jack on the back panel when 6-Channel Mode is selected. **Back Panel**



1. Line-Out (Center & Subwoofer channel) *

2. Line-Out (Rear channels) *

3. Line-Out (Front channels)

* Both Line-In and MIC function are converted to Line-Out function when 6-Channel Mode for 6-Speaker Output is selected.

